

3555 U.S. PTO  
09/997650



438	783
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER
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U.S. UTILITY Patent Application

mm	O.I.P.E.	PATENT DATE
SCANNED	PC	Q.A. JM

VC407

APPLICATION NO. 09/997650	CONT/PRIOR	CLASS 438	SUBCLASS 783	ART UNIT 2813	EXAMINER H. J. ...
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APPLICANTS  
Julia Duncan  
William Minford  
John Osenbach

TITLE  
Semiconductor device having an indium doped dielectric layer located therein and a method of manufacture therefor

PTO-2040  
12/99

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							
H 0 1 L							
H 0 1 L							

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) _____ (Date)  _____ (Primary Examiner) _____ (Date)  _____ (Legal Instruments Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
				<b>ISSUE FEE</b>	
				Amount Due	Date Paid
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